

## **AMENDMENTS TO THE SPECIFICATION**

Please replace the first paragraph on page 1 with the following paragraph:

This patent application discloses subject matter that is related to the subject matter disclosed in United States Patent Application Serial Numbers 09/815,442 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between a First Address Repeater and a Second Address Repeater,” and 09/815,432 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between an Address Repeater and a Client,” filed on even date herein. Each of the above Patent Applications is hereby incorporated by reference.

Please replace the first full paragraph on page 4 with the following paragraph:

Co-Pending United States Patent Application Serial Number 09/815,442 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between a First Address Repeater and a Second Address Repeater” discloses a novel architecture that includes a high-level bus, a plurality of low-level buses, and a novel distributed arbiter. As the efficiency of a computer system that includes the above architecture is dependent upon the proper operation of the distributed arbiter, a need exists for methods of verifying the consistency of the distributed arbiter.

Please replace the second full paragraph on page 8 with the following paragraph:

A block diagram of the L1 address repeater 120 is presented in Figure 2. L1 address repeater 120 includes a plurality of CPU ports 205, 210, and 215. These ports interface with CPUs via the CPU buses 170, 175, and 180. Embodiments of the ports of the L1 address repeater 120, which are shown in Figure 4(a), Figure 4(b), and Figure 5, are further described in United States Patent Application Serial Number 09/815,432 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between an Address Repeater and a Client.”

Please replace the first paragraph on page 9 with the following paragraph:

The CPU arbiter 305, which is shown in Figure 3, is described in United States Patent Application Serial Number 09/815,432 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between an Address Repeater and a Client.”

Please replace the second full paragraph on page 10 with the following paragraph:

Referring again to Figure 3, the L1 address repeater arbiter includes a switch module 315. The switch module 315 is described in United States Patent Application Serial Number 09/815,432 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between an Address Repeater and a Client.”

Please replace the second full paragraph on page 14 with the following paragraph:

The L2 address repeater’s L1 port is described in United States Patent Application Serial Number 09/815,432 entitled “Method and Apparatus for Efficiently Broadcasting Transactions between an Address Repeater and a Client.”